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APPLICANTS:

Rajen Chanchani

SERIAL NO.:

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FILED:

November 13, 2003

EXAMINER:

FOR:

Heterogeneously Integrated System-on-a-Chip

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

November 13, 2003

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Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the examination of this application and in respect of which there may be a duty to disclose.

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2. Form PTO - 1449 (Modified).

3. The person making this statement is the attorney or agent who signs below on the basis of the information supplied by the inventor(s) and the information in the attorney's or agent's file.

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		VENDIER et al., Proc.2001 Electronic Components and Technology Conference, (2001)	
		TOMITA et al., Proc. 2001 Electronic Components and Technology Conference, (2001)	
		TAKAHASHI et al., Proc. 2001 Electronic Components and Technology Conference, (2001)	
		UMEMOTO et al., Proc. 2002 Electronic Components and Technology Conference, 1454 (2002)	
		RAMM et al., Microelectronic Eengineering 37/38, 39 (1997)	
		STANDJORD et al., Int. J. Microcircuits and Electronic Packaging 19(3), 260 (1996)	
		LENIHAN et al., Proc. 1996 Electronic Components and Technology Conference, pp. 119-124, Orlando, May 28-31, 1996.	
		KAMBE, et al., Proc. 41 st ECTC Conference, pp. 14-19, (1991)	
		KOLE et al., Proc., 3 rd Int. Symposium on Adv. Packaging Materials, Atlanta, pp. 71-74 (1997)	
		NIKLAUS, et al., J. Micromech. Microeng. 11, 100 (2001)	

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		SPIESSHOEFER et al., Proc. 2003 Electronics Components and Technology Conference, 631 (2003)	
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